WHAT IS CLAIMED IS:

1. The liquid crystal display comprising:

a first substrate;

a first signal line formed on the first substrate;

a second signal line insulated from and intersecting the first signal line;

a third signal line insulated from the first and the second signal lines;

a pixel electrode formed in a pixel area defined by intersections of the first signal line and the second signal line, the pixel electrode having a plurality of partitions and a plurality of connections connecting the partitions;

a switch connected to the first signal line, the second signal line and the pixel electrode;

a second substrate facing the first substrate;

a common electrode formed on the second substrate; and

a plurality of domain defining members formed on the second substrate,

wherein a first of the plurality of partitions of the pixel electrode has a first side and a second side shorter than the first side, the first and the second sides of the first partition are substantially parallel to the first and the second signal lines, respectively, and the third signal line has a first portion located between the first side of the first partition and the first signal line adjacent to the first side of the first partition.

2. The liquid crystal display of claim 1, wherein a second of the partitions of the pixel electrode has a first side and a second side shorter than

the first side, the first and the second sides of the second partition are substantially parallel to the second and the first signal lines, respectively, and the third signal line further includes a second portion disposed between the first side of the second partition and the second signal line.

3. The liquid crystal display of claim 2, wherein at least one of the first and the second portions of the third signal line partly overlaps the first sides of the partitions of the pixel electrode.

4. The liquid crystal display of claim 3, wherein the third signal line further has a third portion adjacent to the second sides of the partitions of the pixel electrode, the third portion of the third signal line being substantially covered by the pixel electrode.

5. The liquid crystal display of claim 4, wherein the third signal line further has a fourth portion spaced apart by at least 3 from the second sides of the pixel electrode.

6. The liquid crystal display of claim 5, wherein the third signal line is made of the same layer as the first signal line.

7. The liquid crystal display of claim 2, wherein a third of the partitions of the pixel electrode has a first side and a second side shorter than the first side, the first and the second sides of the third partition are substantially parallel to the second and the first signal lines, respectively, and the first to the third partitions are arranged along the second signal lines.

8. The liquid crystal display of claim 1, wherein the third signal line further has a second portion adjacent to the second side of the second partition of the pixel electrode, the second portion of the third signal line being

substantially covered by the pixel electrode.

9. The liquid crystal display of claim 1, wherein the third signal line further has a second portion located between the partitions of the pixel electrode.

10. The liquid crystal display of claim 1, wherein the third signal line is applied with a common voltage which is applied to the common electrode.

11. A liquid crystal display (LCD) comprising:

a first panel having a first field-generating electrode, a first domain defining member, and a signal wire;

a second panel opposite the first panel, the second panel having a second field-generating electrode and a second domain defining member; and

a liquid crystal layer disposed between the first panel and the second panel,

wherein the first and the second domain defining members define a domain wherein molecules of the liquid crystal layer are aligned substantially in a direction, the domain having a planar shape including a first side and a second side, and the first panel further includes an interference protection wire located between the first side of the domain and the signal wire.

12. The LCD of claim 11, wherein one of the first domain defining member and the second domain defining member includes a plurality of partitions for partitioning a corresponding pixel area into a plurality of domains for aligning molecules of the liquid crystal.

13. The LCD of claim 12, wherein the plurality of partitions include a first partition disposed in one of two halves of a pixel area for further dividing

into two domains the one half pixel area and a second and third partition for dividing the second half of the pixel areas into three domains.

14. The LCD of claim 13, wherein the first partition is disposed in a direction transverse to the direction of the second and third partitions.

15. The LCD of claim 11, wherein the interference protection wire is electrically connected to a common electrode.

16. The LCD of claim 15, wherein the pixel area having a pixel electrode is formed on a first substrate in the first panel and the common electrode is formed on a second substrate in the second panel.

17. The LCD of claim 11, wherein the signal wire carries one of gate and data signals.

18. A liquid crystal display (LCD) comprising: a gate wire and a storage electrode wire formed on a substrate and covered with a gate insulating layer; a data wire formed on the gate insulating layer and covered with a passivation layer; a pixel electrode formed on the passivation layer, the pixel electrode is divided into a plurality of partitions, wherein the storage electrode wire is disposed between the partitions and the gate or the data wires.

19. The LCD according to claim 18, wherein the storage electrode wire is spaced apart from a first partition by at least $3\mu m$.

20. The LCD according to claim 18, wherein the plurality of partitions form a plurality of domains in the pixel electrode for aligning liquid crystal molecules therein.